

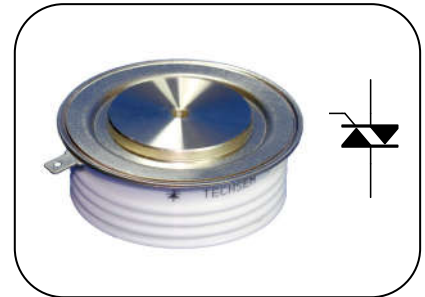
Features :

- Two anti-parallel thyristors on one Si-wafer
- Hermetic metal cases with ceramic insulators
- Capsule packages for double sided cooling

Typical Applications

- High power industrial and power transmission
- DC and AC motor control
- AC controllers

$I_{T(RMS)}$ **750A**
 V_{DRM}/V_{RRM} **500~1800V**
 I_{TSM} **7.6 kA**
 I^2t **288 A²s*10³**



SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _j (°C)	VALUE			UNIT
				Min	Type	Max	
I _{T(RMS)}	RMS current	50Hz sine wave Double side cooled,	T _c =55°C	125		1060	A
			T _c =85°C	125		750	
V _{DRM}	Repetitive peak reverse voltage	tp=10ms	125	500		1800	V
I _{DRM}	Repetitive peak current	at V _{DRM}	125			40	mA
I _{TSM}	Surge on-state current	10ms half sine wave V _R =0.6V _{RRM}	125			7.6	kA
I ² t	I ² t for fusing coordination					288	A ² s*10 ³
V _{TO}	Threshold voltage		125			0.84	V
r _T	On-state slope resistance					1.01	mΩ
V _{TM}	Peak on-state voltage	I _{TM} =900A, F=15kN	25			2.70	V
dv/dt	Critical rate of rise of off-state voltage	V _{DM} =0.67V _{DRM}	125			50	V/μs
di/dt	Critical rate of rise of on-state current	V _{DM} = 67%V _{DRM} to 1000A, Gate pulse t _r ≤0.5μs I _{GM} =1.5A Repetitive	125			50	A/μs
I _{GT}	Gate trigger current	V _A =12V, I _A =1A	25	20		300	mA
V _{GT}	Gate trigger voltage			0.8		3.0	V
I _H	Holding current			20		300	mA
R _{th(j-c)}	Thermal resistance Junction to case	double side cooled Clamping force 15kN				0.035	°C /W
R _{th(c-h)}	Thermal resistance case to heat sink					0.008	
F _m	Mounting force			10		20	kN
T _{stg}	Stored temperature			-40		140	°C
W _t	Weight				240		g
Outline	KT33cT						

Outline:

