

**Features:**

- Isolated mounting base 4000V~
- Pressure contact technology with increased power cycling capability
- Space and weight saving

**Typical Applications:**

- AC/DC Motor drives
- Various rectifiers
- DC supply for PWM inverter

VRRM,VDRM	Type & Outline	
2600V	MTx500-26-410F3	MFx500-26-410F3
2800V	MTx500-28-410F3	MFx500-28-410F3
3000V	MTx500-30-410F3	MFx500-30-410F3
3200V	MTx500-32-410F3	MFx500-32-410F3
3400V	MTx500-34-410F3	MFx500-34-410F3
3600V	MTx500-36-410F3	MFx500-36-410F3
3600V	MT500-36-410F3G	

MTx stands for any type of **MTC, MTA, MTK**  
 MFx stands for any type of **MFC, MFA, MFK**

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	Tj(°C)	VALUE			UNIT
				Min	Type	Max	
IT(AV)	Mean on-state current	180° half sine wave 50Hz Single side cooled, Tc=85°C	125			500	A
IT(RMS)	RMS on-state current					785	A
IDRM IRRM	Repetitive peak current	at VDRM at VRRM	125			55	mA
ITSM	Surge on-state current	VR=60%VRRM,t=10ms half sine,	125			12.5	kA
I²t	I²t for fusing coordination		125			781	10³A²s
VTO	Threshold voltage		125			0.90	V
rT	On-state slope resistance					0.50	mΩ
VTM	Peak on-state voltage	ITM=1500A	25			2.48	V
dv/dt	Critical rate of rise of off-state voltage	VDM=67%VDRM	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	Gate source 1.5A tr ≤ 0.5μs Repetitive	125			200	A/μs
IGT	Gate trigger current	VA=12V, IA=1A	25	30		200	mA
VGT	Gate trigger voltage			0.8		3.0	V
IH	Holding current			20		200	mA
IL	Latching current					1000	mA
VGD	Non-trigger gate voltage	VDM=67%VDRM	125			0.20	V
Rth(j-c)	Thermal resistance Junction to case	Single side cooled per chip				0.054	°C/W
Rth(c-h)	Thermal resistance case to heatsink	Single side cooled per chip				0.024	°C/W
Viso	Isolation voltage	50Hz,R.M.S,t=1min,Iiso:1mA(MAX)		4000			V
Fm	Terminal connection torque(M12)			12		16	N·m
	Mounting torque(M8)			10		12	N·m
Tvj	Junction temperature			-40		125	°C
Tstg	Stored temperature			-40		125	°C
Wt	Weight				3310		g
Outline	410F3						

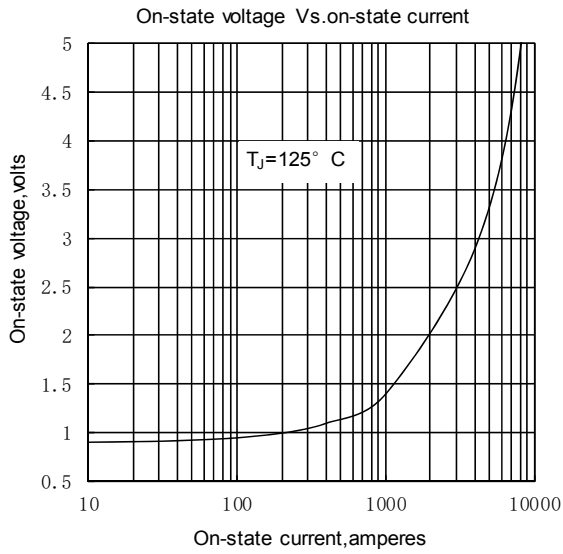


Fig.1

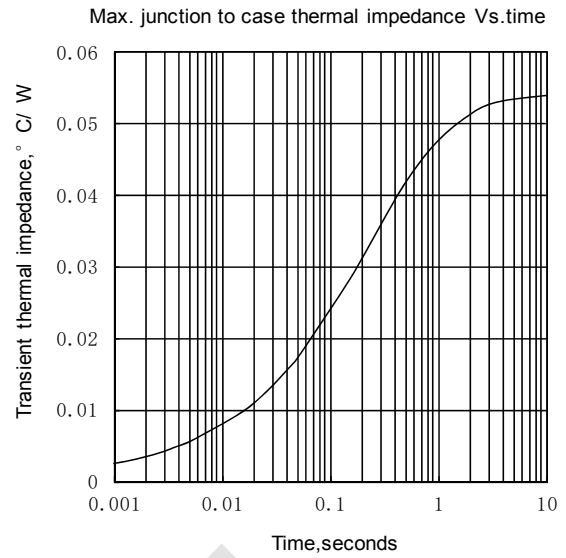


Fig.2

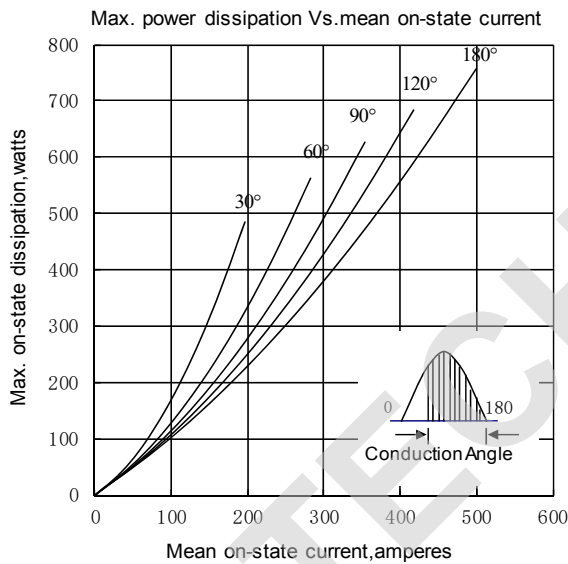


Fig.3

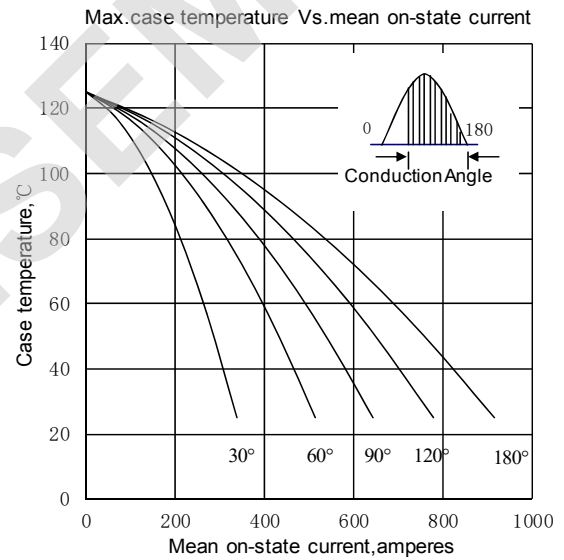


Fig.4

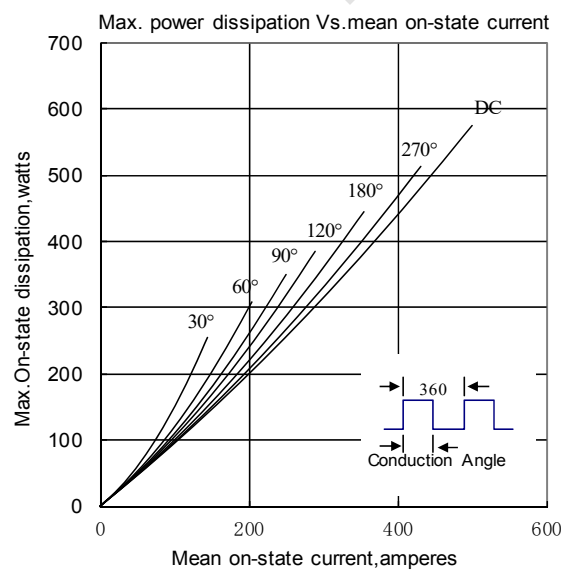


Fig.5

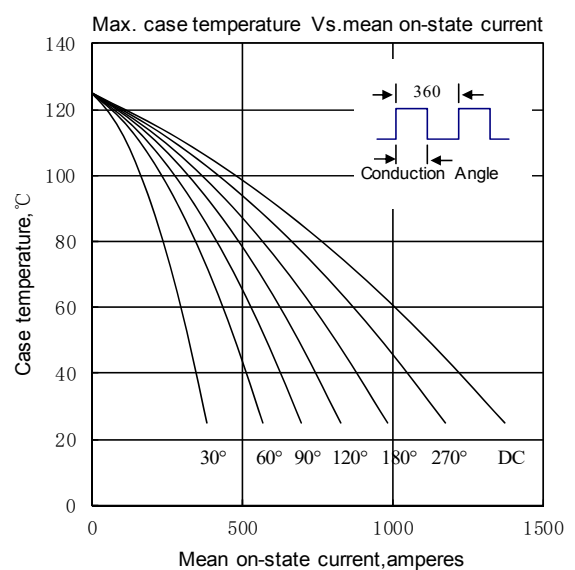


Fig.6

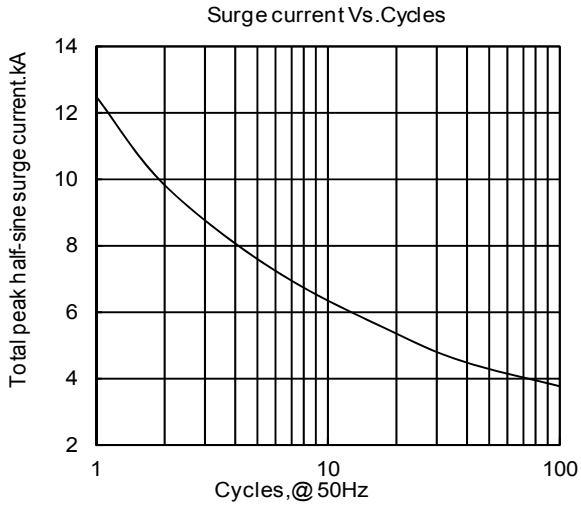


Fig. 7

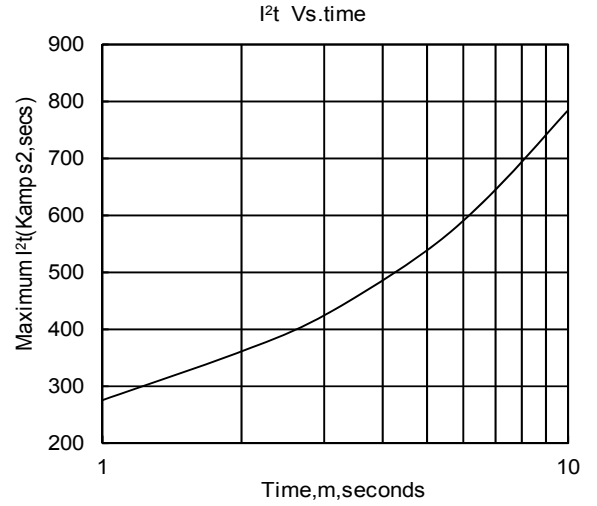


Fig. 8

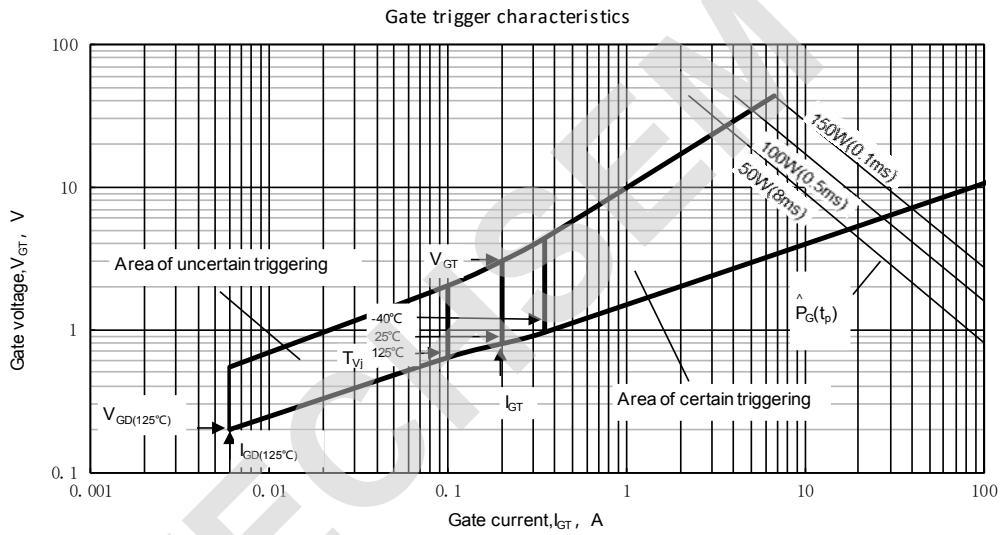
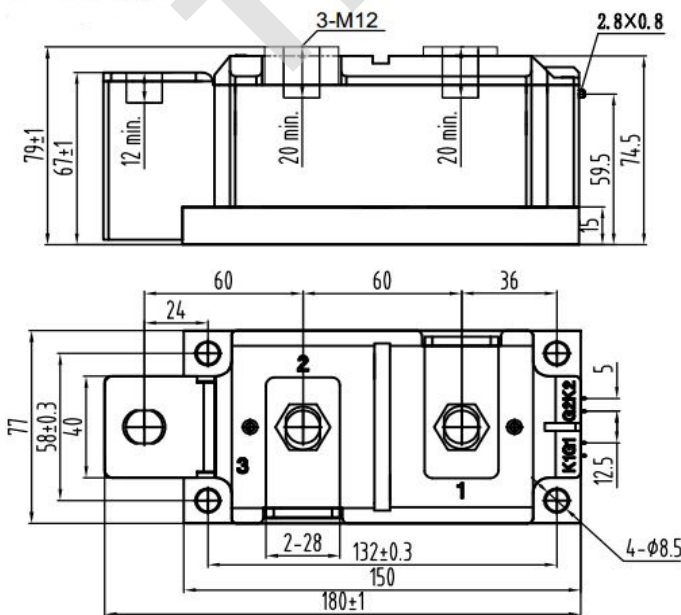
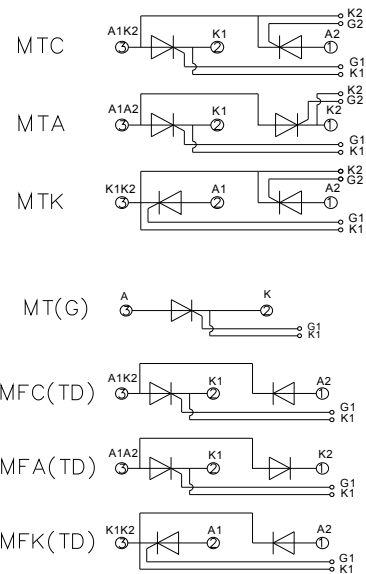


Fig.9

Outline:



Unmarked dimensional tolerance: ±0.5mm



TECHSEM reserves the right to change specifications without notice.