



Features:

- n Isolated mounting base 3000V~
- n Pressure contact technology with Increased power cycling capability
- n Space and weight saving

Typical Applications:

- n AC/DC Motor drives
- n Various rectifiers
- n DC supply for PWM inverter

V _{RRM} , V _{DRM}	Type & Outline
800V	MFx135-08-216F3
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1000V	MFx135-10-216F3
1200V	MFx135-12-216F3
1400V	MFx135-14-216F3
1600V	MFx135-16-216F3
1800V	MFx135-18-216F3

MFx stands for any type of **MFC, MFA, MFK**

SYMBOL	CHARACTERISTIC	TEST CONDITIONS	T _j (°C)	VALUE			UNIT
				Min	Type	Max	
I _{T(AV)}	Mean on-state current	180° half sine wave 50Hz Single side cooled, T _c =85°C	125			135	A
I _{T(RMS)}	RMS on-state current					212	A
I _{DRM} I _{RDM}	Repetitive peak current	at V _{DRM} at V _{RRM}	125			15	mA
I _{TSM}	Surge on-state current	V _R =60%V _{RRM} , t=10ms half sine.	125			3.6	kA
I ² t	I ² t for fusing coordination		125			65	10 ³ A ² s
V _{TO}	Threshold voltage		125			0.80	V
r _T	On-state slope resistance					2.10	mΩ
V _{TM}	Peak on-state voltage	I _{TM} =410A	25			1.75	V
dv/dt	Critical rate of rise of off-state voltage	V _{DM} =67%V _{DRM}	125			1000	V/μs
di/dt	Critical rate of rise of on-state current	Gate source 1.5A t _r ≤ 0.5μs Repetitive	125			200	A/μs
I _{GT}	Gate trigger current	V _A =12V, I _A =1A	25	30		150	mA
V _{GT}	Gate trigger voltage			0.7		2.5	V
I _H	Holding current			10		150	mA
I _L	Latching current					1000	mA
V _{GD}	Non-trigger gate voltage	V _{DM} =67%V _{DRM}	125			0.20	V
R _{th(j-c)}	Thermal resistance Junction to case	Single side cooled per chip				0.20	°C/W
R _{th(c-h)}	Thermal resistance case to heatsink	Single side cooled per chip				0.08	°C/W
V _{iso}	Isolation voltage	50Hz, R.M.S, t=1min, I _{iso} : 1mA(MAX)		3000			V
F _m	Terminal connection torque(M6)			4.0		6.0	N·m
	Mounting torque(M6)			4.0		6.0	N·m
T _{vj}	Junction temperature			-40		125	°C
T _{stg}	Stored temperature			-40		125	°C
W _t	Weight					320	g
Outline	216F3						

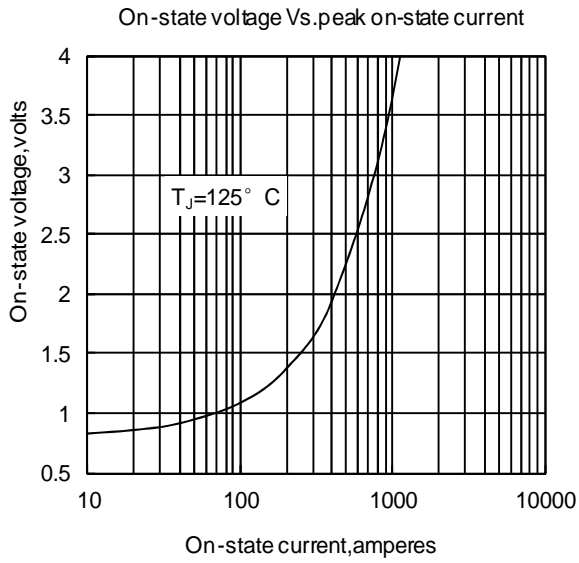


Fig1

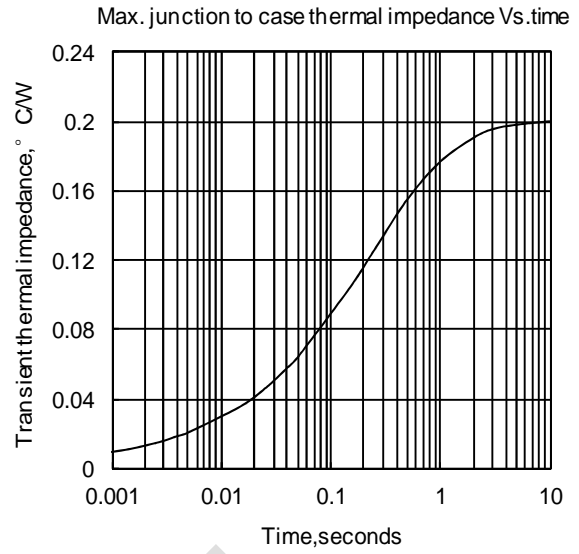


Fig2

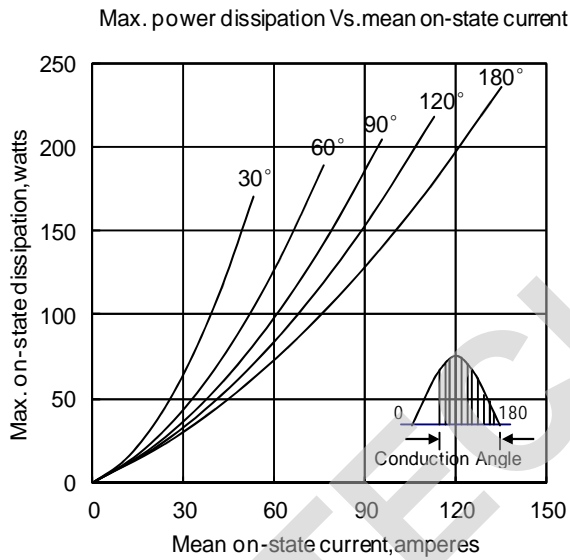


Fig3

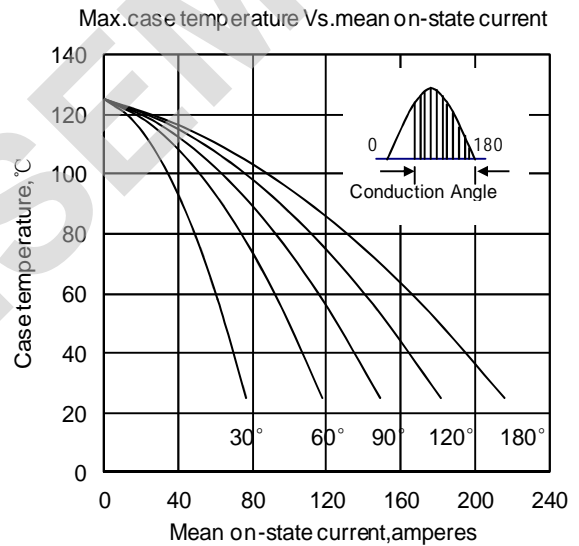


Fig4

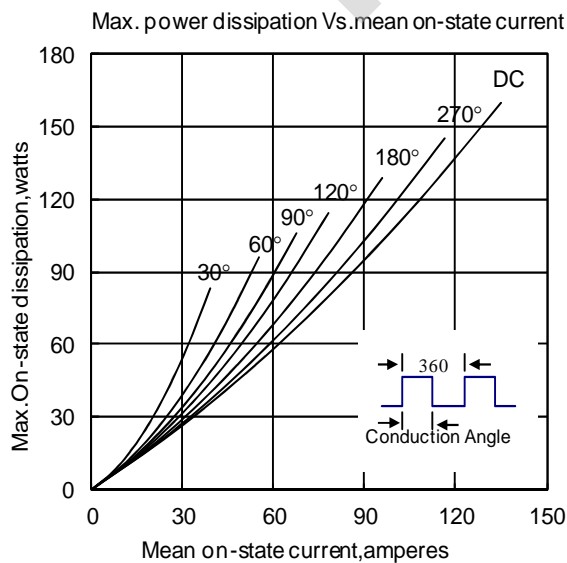


Fig5

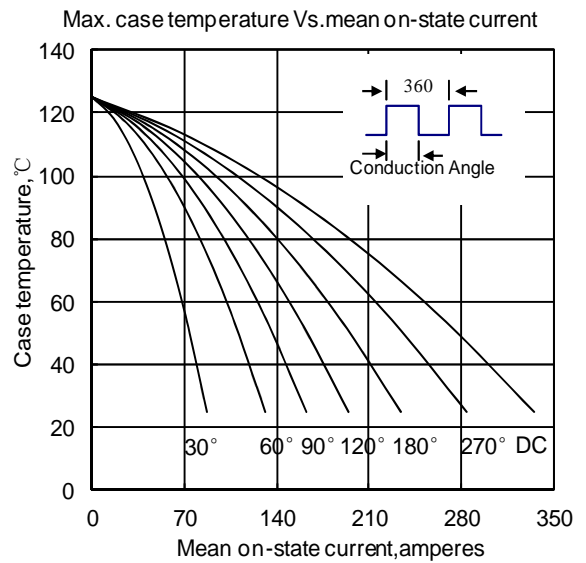


Fig6

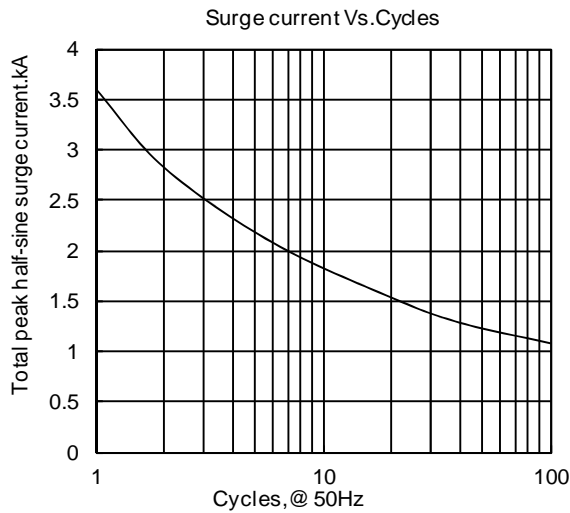


Fig7

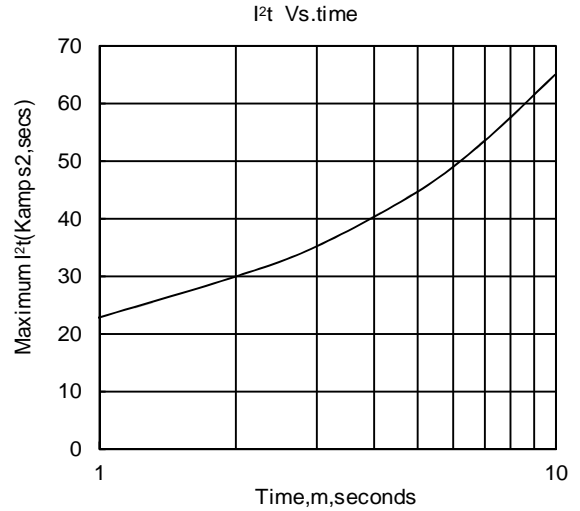


Fig8

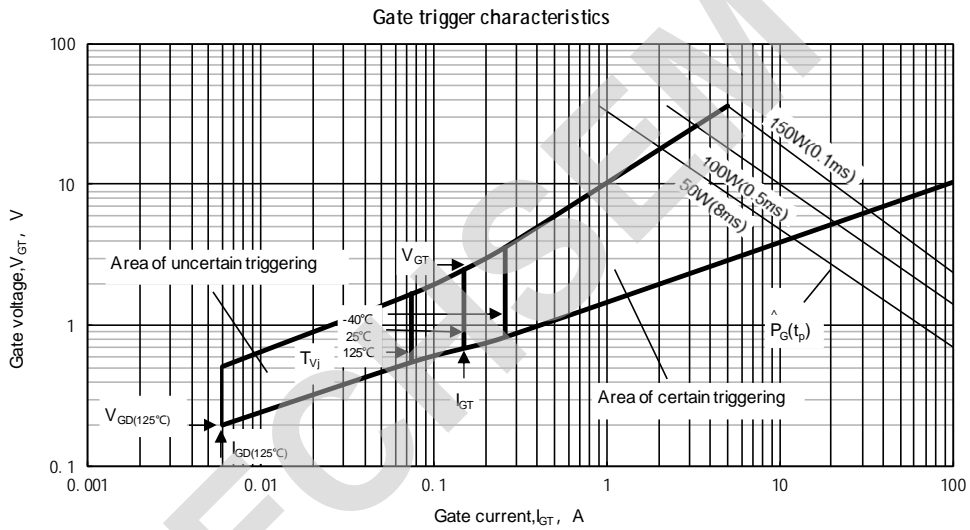
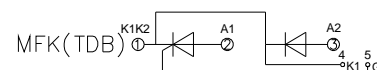
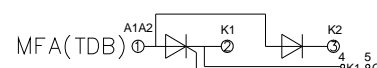
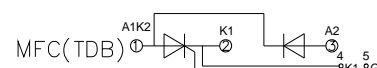
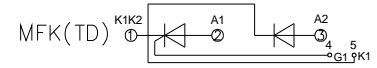
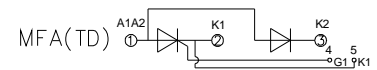
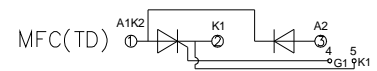
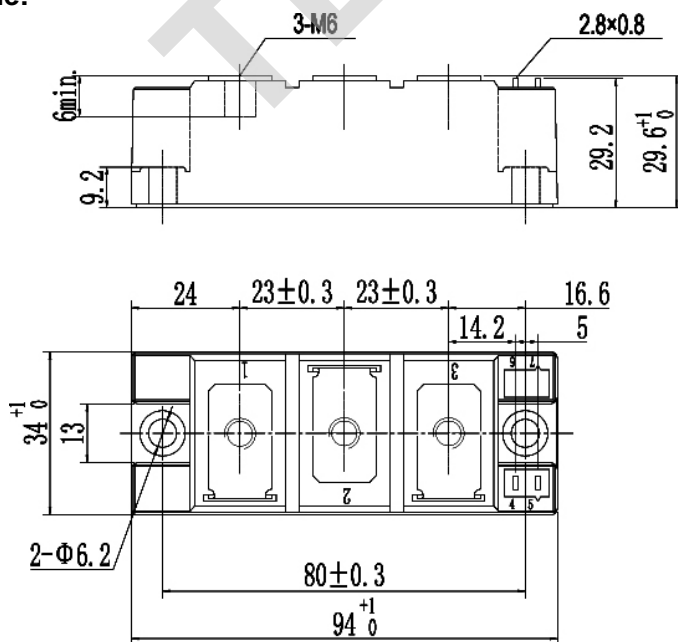


Fig.9

Outline:



Unmarked dimensional tolerance: ±0.5mm

TECHSEM reserves the right to change specifications without notice.